

Impact of DC grid contingencies on AC system stability

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Abstract

In order to reliably and safely operate future multi-terminal HVDC grids, different protection strategies are currently being developed and presented in the literature. Especially for large DC grids, it is important to investigate how the choice of the protection strategy influences AC system stability during DC fault clearance and system restoration. This paper contributes to the understanding of these issues by giving an overview of the different protection strategies and their influence on frequency and transient stability. DC grid protection requirements are defined, based on the AC system constraints related to the level and duration of power transfer loss.

1 Introduction

High voltage direct current (HVDC) technology is expected to play a crucial role in the development of future power systems. Especially for transporting large amounts of power over long distances, for connecting different asynchronous zones or to connect offshore wind farms with mainland systems, HVDC is the preferred option as it offers some benefits, e.g. the simpler use of long cables and its controllability compared to AC technology [1].

To this day, HVDC is mostly being used for point-to-point connections. However, by interconnecting several HVDC links, a multi-terminal DC grid is formed which can be embedded into a single AC grid or linking multiple asynchronous AC systems. Voltage source converter (VSC) HVDC is considered to be the most suitable converter technology for building such a grid since it allows the connection of weak islanded grids and makes it feasible to easily build and control parallel extensions.

The gradual expansion of the existing point-to-point VSC HVDC links toward DC grids poses new challenges that need to be studied. In this paper, the impact of DC grid contingencies on the stability of the associated AC systems is investigated. The level of impact is influenced by the size and structure of the DC grid, the structure of the associated AC grids, the type and duration of the DC Grid contingencies, as well as the type of DC grid protection strategy chosen.

Depending upon the required fault clearing time, the applied converter technology (fault blocking or non-fault blocking) and the available DC grid protection equipment, multiple protection strategies can be adopted to detect and interrupt DC faults, disconnect the affected elements or protection zone and to ensure a stable operation of the remaining system.

From an AC system's perspective, the contingency and accompanied protection strategy could lead to a temporary or permanent power imbalance as well as loss of AC voltage support to the associated AC grids. Each of these elements can impact the AC system stability.

The aim of this work is therefore to shed light on the impact of HVDC grid contingencies on the AC system stability, as well as how this relates to the choice of the DC grid protection strategies.

The structure of the remainder of the paper is as follows: It starts by giving an overview of the different DC contingencies and the AC system stability in section 2. The impact of loss of power transfer from the DC grid on the AC grid frequency stability is briefly demonstrated. Thereafter, in sections 3 and 4, the different strategies to clear a fault in the DC grid and to restore the system after an outage are addressed and it is described how the power exchange with the AC system is affected for each protection strategy chosen, in terms of loss of maximum power transfer and the duration. Finally, conclusions and recommendations for future research are given in section 6.

2 DC grid contingencies and AC system stability

2.1 DC contingencies

A DC contingency can be defined as a non-functionality of a component within the DC grid such as converter, DC transmission line or busbar, or a non-functionality of the DC grid itself. In this study, the impact of DC contingencies such as converter outages and DC transmission line faults on AC system stability are analyzed. The impact of DC contingencies on AC system stability depends on the topology of both the AC and DC system, the contingency type and the employed AC and DC protections.

Future DC grids are expected to be built step by step from ex-

isting point-to-point links to form small separate DC grids, and grow into large-scale supergrids through interconnections [1]. Thus the connection between AC and DC systems are assumed to have various forms, such as DC grids embedded within one synchronous AC grid or alternatively DC grids interconnecting different synchronous AC grids, including offshore wind farms. Fig. 1 gives an example of such a possible connection between the AC and DC grid, where a DC grid is connected with three AC systems. AC_2 and AC_3 are asynchronous systems, while AC_1 has three synchronous areas. We will use Fig. 1 to explain the problems encountered in the AC grids during DC contingencies.

- **Outage of a converter:** The power exchange of the converter at the associated node is lost in case of a converter outage. This power exchange is compensated through power droop controls of the other converters in the DC grid, resulting in a near equal change in those converters. If all converters are connected to one synchronous zone, this can be considered as loss of power transfer. If multiple zones are involved, a positive or negative power imbalance will also be created depending on the direction of the exchange.
- **DC faults:** The impact of a DC fault on the AC and DC grids is directly related to the adopted DC protection strategy. For an AC grid with an embedded DC grid, a DC fault will lead to a temporary loss of power transfer regardless of the adopted DC protection strategy. However, the severity (amount and duration) will depend on the relative size of the DC grid and the DC protection strategy. In the case of a dc grid connecting asynchronous AC grids, a DC fault can result in loss of power in-feed/load, loss of power transfer or combination of both depending on the fault location and adopted DC protection strategy.

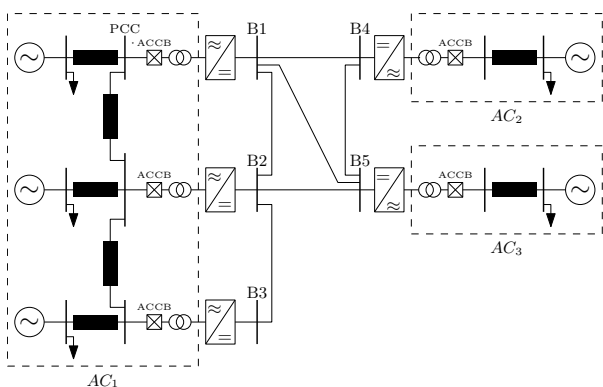


Fig. 1: Example of AC and DC system configuration.

2.2 Impact on AC system stability

The stability of an AC power system is in general defined as the ability of the system to regain a state of operating equilibrium after being subjected to a disturbance, with most system

variables bounded so that the entire system remains intact [2]. Looking at the system variable in which the instability can be observed, power system stability is further classified into voltage, frequency and rotor angles stability. In this work, the main focus is on these latter two forms which are linked to the electromechanical dynamics within a power system due to the oscillation of the rotating masses of the generators following a disturbance. These dynamics are mainly influenced by the active power transfers in the system and can, due to the inertia provided by the synchronous machines, be considered relatively slow in nature compared to the dynamics within the DC grid. The time frame of interest is mostly in the order of hundreds of milliseconds to seconds.

For point-to-point DC cable links between two distinct AC systems, a contingency at the DC side will always result into a permanent loss of power transfer to the AC side. Considering DC grids on the other hand, the faulty DC component can be isolated and the total power exchange with the AC system may be rapidly restored depending on the applied protection, control strategy and the capacity of the remaining grid.

These fast changes in the power transfer at different terminals might however entice badly damped modes which lead to large rotor angle oscillations between groups of generators that are electrically located far apart from each other within one synchronous zone, possibly resulting in a loss of synchronism. Different factors such as the network configuration, the type and control of the generators, their initial loading and the control of the converters will have an influence.

Considering frequency stability, the network configuration is of less importance and the frequency response for a certain power loss in a synchronous zone due to a DC contingency is determined by the inertia and governor control of the operating power plants, the type of load as well as the behavior of other power electronic coupled generation units. For point-to-point connections, a limit is often imposed on the maximum power injection in order to keep the frequency within predefined boundaries in case the power transfer is lost. For a DC grid with multiple terminals connected to the same synchronous zone, this limit could be increased as the power imbalance within the system can be (partially) restored by altering the power setpoints of the remaining converters.

These phenomena are illustrated in Fig. 2. For the results in Fig. 2, a simple power system equivalent for frequency control analysis is used in order to model the frequency response following a sudden loss of active power infeed (ΔP_{HVDC}) from the DC grid during a certain amount of time, represented by (ΔT_{HVDC}) as shown in Figure 2(a) [3]. The minimum frequency and rate of change of frequency (ROCOF) directly after the event are given in Figure 2 for different inertia values (H). The total equivalent droop (R) and primary control time constant is taken equal to respectively 0.05 and 1s. Furthermore, it is assumed that sufficient primary reserve is available to cope with the simulated loss of power from the DC system.

As expected, the ROCOF predominantly depends on the power

imbalance and system inertia. By contrast, the minimum frequency can be increased by rapidly restoring the power exchange with the system. An important consideration is that, to increase the frequency compared to the case of permanent power loss, the restoration of power should occur within a certain time after the contingency, as indicated by the dashed line in Figs. 2(c) and 2(d). Especially for systems with low inertia, this time period is very short, possibly imposing a limit to the maximum available time to restore the DC system.

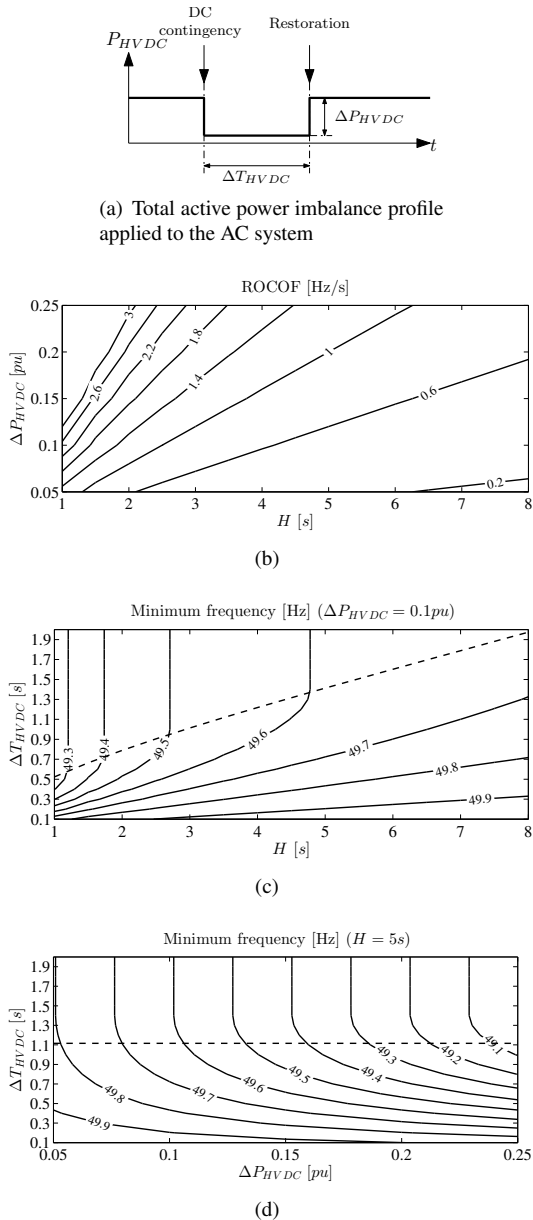


Fig. 2: Minimum and rate of change of frequency (ROCOF) as function of system inertia, magnitude and duration of power imbalance (system droop $R = 0.05$ and equivalent primary control time constant $T = 1 s$), system base is taken equal to the total generation capacity.

3 DC network fault clearance and restoration

In the literature, several DC fault clearing strategies are proposed and can be classified as selective, partly-selective and non-selective strategies [4]. In this paper, we focus on the selective and non-selective strategies, since the impact of partly-selective strategies, such as grid-splitting, can be considered as intermediate.

3.1 Selective protection strategy using DC circuit breakers

The selective DC fault protection strategy is similar to what is currently been done in AC transmission system, i.e., isolating only the faulted line by utilizing DC circuit breakers. The total time available for fault clearing typically is in the order of several milliseconds due to the high rate of rise and large steady-state fault current [5]. In the literature, both unit [6, 7] and non-unit protection schemes [8–11] for meshed HVDC grids have been proposed. Because of the requirement on the speed of operation, non-unit schemes and unit schemes with fast communication are considered to be applicable in HVDC grids protection.

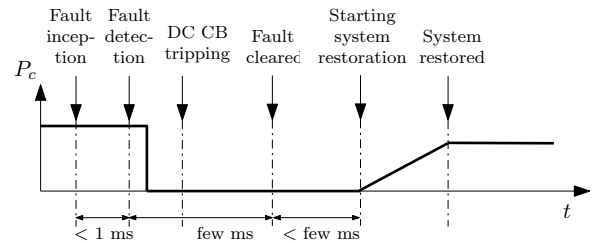


Fig. 3: Selective protection strategy using fast DC circuit breakers.

Fig. 3 shows the steps and time scales involved in a selective protection strategy considering non-unit fault detection algorithms [8], [10], and [11]. Upon arrival of the incident wave at the relay position, it takes less than 1 millisecond to detect and discriminate the faulted line by currently proposed algorithms. Then the tripping signal will be sent to the designated DC circuit breaker. The operating time of hybrid DC circuit breakers is in the range of 2 ~ 5 ms [12], [13], and 8 ~ 10 ms for active resonant DC circuit breakers [14]. Once the fault is cleared by the circuit breakers, the DC grid is ready to be restored.

Depending on the passive elements (e.g. fault current limiting inductor), the fault location, and the operating speed of the DC circuit breaker, the converters close to the fault location may be temporarily blocked during the fault clearing process. If the converters, especially the ones at the healthy terminals are blocked, algorithms or sequences in order to de-block and re-synchronize the converters are necessary in order to restore the DC grid. The duration for power restoration is typically in the order of milliseconds without converter blocking, and may be longer if converters are blocked during the fault clearing process. The impact on the AC system stability, therefore, is

expected to be insignificant if fast power restoration can be achieved.

3.2 Non-selective protection strategy using AC circuit breakers for fault clearing

In a second strategy, the fault current is interrupted by opening all circuit breakers installed at the AC side of the converters. A communication-less protection scheme as for instance presented in [15] can be adopted in order to reliably clear the fault, isolate the faulted DC line and subsequently restore the entire system.

In this case, only relatively cheap DC switches are installed at both ends of each DC line and no DC circuit breakers are required which reduces the investment cost of building multi-terminal HVDC grids [16]. However, this strategy leads to long fault clearing times as it may take up to a few cycles of the fundamental AC frequency to interrupt the fault current by the AC circuit breaker (40-80 ms) [4]. Additionally, due to the non-selective fault interruption method, the entire DC network will be de-energized before the fault is cleared [17].

In Figure 4, the different steps and time scales involved in this method are schematically presented. Also the variation of the active power of a single converter (P_c) and the AC voltage (U_{PCC}) at the point of common coupling (see also Figure 1) is roughly illustrated.

When the fault occurs, the current in each converter will not vary instantaneously, but a very small time delay exists before the fault reaches each converter due to the finite speed of the traveling wave from the fault location to the converter terminals. This time delay depends mainly on the fault location, grid layout and network parameters and is mostly in the order of milliseconds. Once the converter detects the fault and the current reaches a certain threshold value, the converter protection will block the converter in order to prevent damage the IGBTs, which have only a limited overcurrent capability. At the same time, a tripping signal is send to the ACCBs.

Due to the blocking of the IGBTs, the current is commutated to the anti-parallel diodes and the converter becomes an uncontrolled rectifier (assuming a converter with half-bridge technology). This leads to a large infeed from the AC system which will, together with the current from the discharge of the DC capacitors and cables, determine the total fault current. Eventually, the fault current reaches a steady state value, predominantly determined by the infeed from the AC system [17]. The AC terminal voltage has by now dropped significantly, to a value approximately equal to the voltage drop across the phase reactor and arm inductor.

Within the time period between the fault detection and operation of the AC circuit breaker, the faulted DC branch is identified using local current and voltage measurements [15]. As soon as the current through the faulted line has dropped to zero, the fault is isolated by opening the designated DC switches. In the meantime, also the voltage at the AC side is raised or

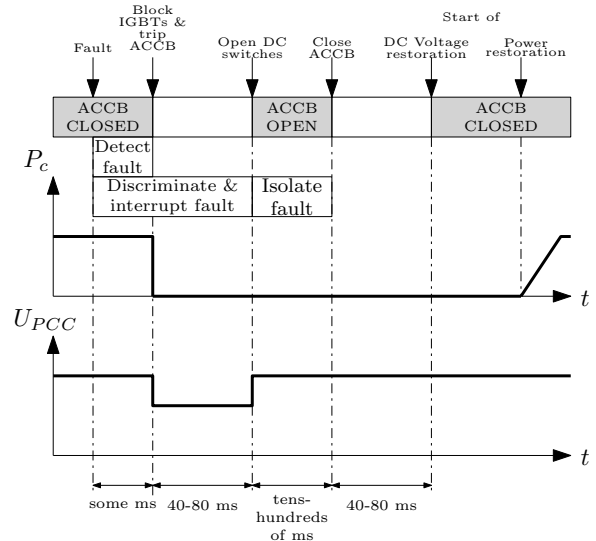


Fig. 4: ACCB strategy.

restored to its nominal value, depending on the voltage regulation capability of the AC system. Finally, the DC grid is reconnected by the ACCBs in order to regain pre-fault system operation. The time required to restore the system is highly depending on the applied converter control and system layout. However, as it corresponds to a complete DC grid restart including ramping up the power, it can possibly take seconds or even minutes to fully restore the system.

From the point-of-view of an AC system, the DC grid fault can be considered equivalent to multiple short-circuits at each converter terminal as soon as the IGBTs of the converters are blocked [16]. Due the voltage drop within the AC system and the long fault clearing times, transient stability issues can therefore occur, possibly resulting in the loss of synchronism of the synchronous generators connected to the system.

Furthermore, since the power exchange with the AC system is lost during several seconds, the frequency stability may be jeopardized depending on the strength of the AC system in terms of inertia and on the primary control capability. Large frequency deviations may occur which might result in load shedding or even lead to a complete black-out.

3.3 Non-selective protection strategy using converters with DC fault blocking capability for fault clearing

The utilization of the modular multi-level converter (MMC) with DC fault blocking capability in the multi-terminal DC grid represents the third fault clearing strategy considered in this paper. Various converter typologies with DC fault blocking capability were presented in [18] and [19]. The blocking capability of the converter is exploited to control the DC current by inserting negative voltages or to interrupt it by blocking all converters in the grid [20]. As the required time to insert negative voltages by the submodules (to control the DC current) or to turn off the submodules (to interrupt the DC fault

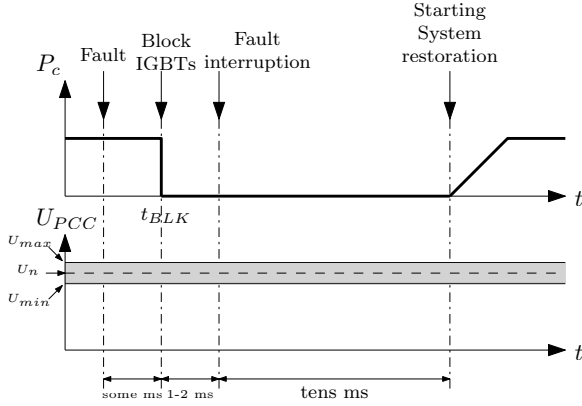


Fig. 5: Fault clearance strategy by converters with DC fault blocking capability.

current) is within 1-2 ms, the fault current interruption is in the range of 1-2 ms as well [21]. Hence, fast DC circuit breakers are not required. However, residual current breakers (RCB) can be included at both ends of each DC line, to clear the residual currents after the converters' blocking and to isolate the faulty line in order to restore the system safely [22]. Similar to the AC circuit breakers strategy, the protection system is used for this strategy to detect and identify the faulty line.

Figure 5 demonstrates the stages of fault clearance by converter blocking. Furthermore, it shows two sketches for the exchanged active power (P_c) and the AC voltage at the point of common coupling (U_{PCC}). The fault clearance period is divided into three intervals; the first interval represents the time before the detection of the fault, while the second interval starts at the instant of detection and blocking of the converter. The restoration of the system operation is initiated in the third interval.

During the first interval, the voltage at the point of common coupling and the exchanged active power will not be influenced, even after the arrival of the fault wave at the DC terminal of the converter, since the converter is still in operation [21]. However, the submodules will be turned off once the current in the converter exceeds a specific threshold at t_{BLK} , and the converter turns to blocking mode. Consequently, the power drops to zero as illustrated in Figure 5. Thus the AC system will see the converter blocking as a disconnection of a heavy load or as a disconnection of a generation unit.

Although the shut down of the entire DC grid is similar to the previous strategy, the time scale of current interruption and restoration is much shorter compared with the AC circuit breaker strategy. The starting instant of system restoration depends on the size of the HVDC grid and the speed of the RCBs to isolate the faulty part. In [22], two types of RCBs were examined (RCB with 2ms and 30ms opening time). The required time to unblock the converters and restore the grid operation is directly proportional to the the speed of the installed RCBs.

The capability of this type of converters is not limited to inter-

rupt the DC current, it can be extended to supply the required reactive power to the AC grid (STATCOM operation mode) even during DC side faults by reducing the exchanged power to zero and keeping the converter in operation as proposed in [19] and [23] to support the AC grid.

4 Converter outage and droop control

Contrary to a fault on the DC grid which can be cleared using one of the aforementioned strategies, a fault within the converter or on its DC bus bar will mostly result in a permanent loss of the converter. This loss is immediately reflected by an increase or decrease in the DC voltage due to the (dis)charging of the cables and DC capacitors.

In order to stabilize this DC voltage within the system, the power imbalance may be compensated by a single (slack) converter or by multiple converters using a distributed DC voltage droop control scheme [24]. Different converters will in this case jointly react upon the changing DC voltage by altering their power set points, similar to the primary control action used in AC systems.

The time scale though of the DC voltage stabilization is in the range of tens of millisecond, i.e. a couple of orders of magnitude smaller than the primary control action within the AC system. The power sharing amongst the converters participating in this droop control can further be tuned by changing the droop constants.

In case each converter is linked to a separate AC system, the outage of a converter and the accompanied control action of the remaining converters will therefore lead to a fast variation in the power output. Hence, from the point-of-view of each AC system, it can be considered equivalent to the loss of a single load or generation unit which will directly affect the AC system frequency. Weak AC systems with low inertia, insufficient reserve capacity or slow governor control will encounter large frequency transients, leading to load shedding or disconnection of renewable generation units, possibly aggravating the initial frequency event. Therefore, in order to ensure the frequency to stay within the boundaries after an outage, a limit may be imposed to the maximum power that is transferred in steady-state.

When considering an HVDC grid in which multiple DC converters are linked to one interconnected AC system, the total power exchange with the DC grid may be rapidly restored, improving the frequency response after the outage as shown in section 2.2. However, due to the parallel paths that exist at the AC and DC side, complex interactions will occur between both systems. Therefore, care should be taken in selecting the droop constants in order to not compromise system stability. To this end, a methodology which optimizes the droop settings in order to minimize the adverse effects on the AC system as for instance presented in [25] can be applied.

5 Discussion

As described in the previous sections of the paper, the time frame of DC contingencies is much shorter than those in AC systems. Furthermore, as illustrated in section 2.2, the minimum frequency during a contingency can be increased if the loss of power is rapidly restored. However, the current AC grid code is determined based on permanent loss of power infeed which might not be the most appropriate for the future overlay AC/DC grids. One possible suggestion is given in Fig. 6, where the maximum allowed loss of power infeed P_{max} is in accordance with the current AC grid code. However, this level could be higher if the power could be restored in shorter times. The maximum loss of power infeed could be defined as multiple steps, transient loss (restoration within t_1 e.g. less than one cycle), temporary loss (restoration within t_2 e.g. hundreds ms), and permanent loss (restoration longer than t_3). In this paper, an equivalent single machine model is used to illustrate the impact of various durations; however, more detailed models are needed to define the different characteristics shown in Fig. 6 for a given system. Consequently, once the maximum loss of power infeed and duration is defined from the surrounding AC systems, the protection strategy of a DC grid would have to be designed to fulfill these requirements.

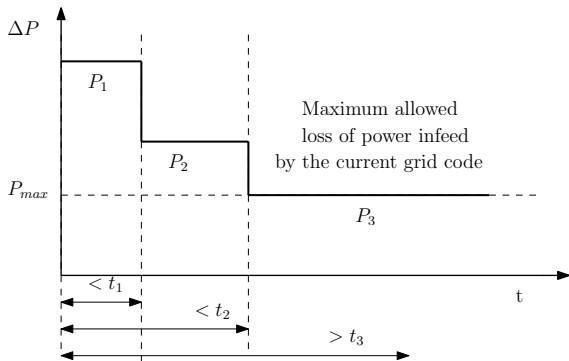


Fig. 6: Possible future AC grid code: loss of power infeed and duration.

6 Conclusions

The stability of the AC system during DC contingencies is mainly influenced by the impact of these contingencies in terms of power imbalance and duration. These are directly influenced by the adopted DC protection strategy, the type of contingency and its location. Three fault clearance strategies were discussed in terms of time scale and their impact on the AC grid stability.

The selective protection strategy might not have an impact on the frequency and transient stability, as fast DC circuit breakers are used to isolate the faulty part. The duration of the fault detection, clearance and system restoration is in the range of tens of milliseconds which is considered to be too short to have an influence on the AC grid stability.

On the contrary, the AC circuit breakers strategy for fault clearance results in de-energization of the DC grid for long period of time. Due to the long period of fault clearance and system restoration, the AC grid can encounter transient stability problems if the size of the DC system is relatively large compared to the AC system.

Considering the converters with DC fault blocking capability, the restoration time of the whole system depends on the size of the DC grid. In addition, the speed of operation of the installed RCBs plays a main role in determining the required time to initiate the system restoration. However, the restoration time of non-selective strategy using converters with DC fault blocking capability is shorter than non-selective strategy using AC circuit breakers.

Moreover, the impact of the converter outage depends on the AC/DC grid topology. In case of each converter connected to a separate AC grid, the maximum amount of power transfer loss by each converter should be limited to ensure the frequency stability during the converter outage, while the droop constants should be carefully selected in case of embedded DC grid within one synchronous AC grid or multiple converters connected to one AC grid, in order to minimize the impact on the AC system.

Finally, a future AC grid code has been proposed which defines the maximum level and duration of power transfer loss based on the AC system constraints. Further research is however required to determine the specific shape of this curve which will set a limit to the maximum available time to restore the power transfers between the AC and DC grid.

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